

REMARKS

Claims 1-7 are pending in the application, wherein Claims 1 and 5 are independent claims. The Examiner has rejected Claims 1 and 5-7 under 35 U.S.C. §102(e) as being anticipated by *Adachi* (U.S. Pub. 2002/0155822); Claim 2 under 35 U.S.C. §103(a) as being unpatentable over *Adachi* in view of *Kataoka et al.* (JP 10247953); and Claim 4 under 35 U.S.C. §103(a) as being unpatentable over *Adachi* in view of *Mitama* (EP 0863606A1). The Examiner has objected to Claim 3 as being dependent upon a rejected base claim, but stated that it would be allowable if rewritten in independent form including all limitations of the base claim and any intervening claims.

It is respectfully submitted that the Examiner is incorrect in rejecting Claims 1, 5-7 under 35 U.S.C. §102(e) based on *Adachi*. *Adachi* neither discloses nor suggests each and every element of independent Claims 1 and 5, and thus it does not anticipate the claims.

Unlike the present invention, *Adachi* neither suggests nor discloses mixing two I/Q components separated by 180° in phase according to a modulated signal received from an antenna with a signal provided from a low noise amplifier, in mixers, and then eliminating spurious signals generated in the mixers through low pass filters (LPF), and compensating a direct current (DC) offset signal through a first amplifier having a fixed gain and a second amplifier having a variable gain. Still further, *Adachi* neither discloses nor suggests if the detected signal is output into feedback loop circuits after detecting a difference between two DC offset signal components in a detecting means by using a signal output from the mixers, feedback loop circuits, output control voltage for adjusting the DC offset to zero according to the detected information, into a second amplifier having a variable.

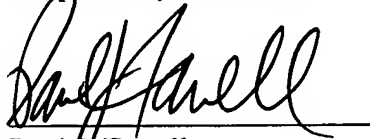
On the contrary, with reference to Fig. 2 of *Adachi*, the Examiner states that adders (113, 114) of Fig. 2, which generate an I-ch baseband signal and a Q-ch baseband signal by receiving an input of a baseband signal having an anti-phase relation, correspond to the detecting means and the adjusting means as recited in Claim 1. That is, the adders (113, 114) of the cited reference, which, the Examiner states are equivalent to the detecting means and the adjusting means of the present invention, just generate the I-ch baseband signal and the Q-ch baseband signal, but do not detect a difference between two DC offset signal components or reduce the difference by an adjusting means, as disclosed in independent Claims 1 and 5 of the present invention.

Given the above, *Adachi* neither discloses nor suggests each and every element of amended independent Claim 1 and 5, and thus it does not anticipate the claims. Consequently, independent Claims 1 and 5 are distinguishable over *Adachi* and are believed to be in condition for allowance.

Therefore, without conceding the patentability of dependent Claims 2-4 and 6-7, these claims are believed to be patentable based on at least their respective dependency from independent Claims 1 and 5. Neither the combination of *Adachi* and *Kataoka et al.* nor *Adachi* and *Mitama* is sufficient to overcome the shortcomings of *Adachi*. Accordingly, reconsideration and withdrawal of the rejection of Claims 1-7 are respectfully requested heretofore.

The application as now presented, containing Claims 1-7 are believed to be in condition for allowance. Should the Examiner believe that a telephone conference or personal interview would facilitate resolution of any remaining matters, the Examiner may contact Applicant's attorney at the number given below.

Respectfully submitted,

A handwritten signature in black ink, appearing to read "Paul J. Farrell", written over a horizontal line.

Paul J. Farrell

Reg. No. 33,494

Attorney for Applicants

DILWORTH & BARRESE, LLP
333 Earle Ovington Boulevard
Uniondale, New York 11553
TEL: (516) 228-8484

PJF/SNB/mk